

VLSI circuits are too complex to design without CAD tools since the complexity of many VLSI circuits is in the order of millions of transistors today. In order to reduce the complexity of the design process for a VLSI circuit, the whole process is broken into several intermediate phases. Among them is a physical design phase. In the physical design phase, the basic components of the circuit are usually thought of as rectangular modules. The physical design phase itself consists of several steps. One of the steps, which is most crucial for the performance of the circuit, is a floorplan/placement step. Actual floorplan/placement problem in the VLSI circuit design is very complex. However, here, you are asked to deal with a simple version of the problem, in which each component of a VLSI circuit can be viewed as a rectangle in the plane.

A simple version of the floorplan/placement problem is to place n rectangles in the plane in axis-parallel orientation such that some given constraints are satisfied. Information about relative locations among rectangles is given as constraints. Relative location means that rectangle R_i ($1 \leq i \leq n$), should be placed either below (or above) rectangle R_j ($1 \leq j \leq n$), or to the left (or right) hand side of rectangle R_j . For each rectangle R_i , the coordinates of its lower left corner and its upper right corner are represented as (x_i^{ll}, y_i^{ll}) and (x_i^{ur}, y_i^{ur}) , respectively. That rectangle R_i is located below rectangle R_j means $y_i^{ur} \leq y_j^{ll}$. Similarly, that rectangle R_i is to the left of rectangle R_j means $x_i^{ur} \leq x_j^{ll}$. Notice that each rectangle can be placed rotated by 90° . Figure 1 shows two example cases in which rectangle R_1 is below rectangle R_2 .

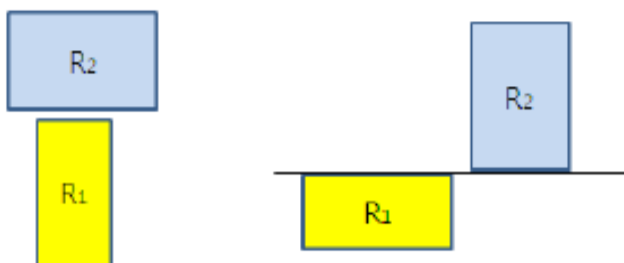


Figure 1. Two example cases to illustrate R_1 is below R_2 .

The constraints regarding relative locations among the rectangles are represented as a binary tree called ‘slicing tree.’ A slicing tree represents how the plane is partitioned and which rectangle should be placed in each sub-region. Each internal node of the slicing tree is labeled as either H or V. Each external node of the slicing tree is labeled with a rectangle identification number i ($1 \leq i \leq n$). The label H or V for an internal node means that the sub-region on the plane is partitioned either by a horizontal line or by a vertical line. For example, if the slicing tree is as shown in Figure 2, it means that the plane is partitioned by a horizontal line and that rectangle R_1 should be placed below rectangle R_2 . Notice that both of the placements shown in Figure 1 meet the constraint shown in Figure 2.

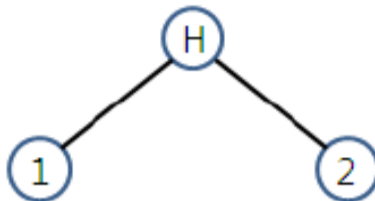


Figure 2. An example of slicing tree for 2 rectangles

On the other hand, if the slicing tree is as shown in Figure 3, it means that the plane is partitioned by a vertical line and that rectangle R_1 should be placed to the left hand of rectangle R_2 .

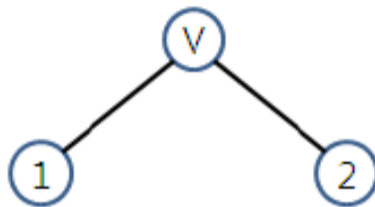


Figure 3. Another example of slicing tree for 2 rectangles

If any internal node N_k in the slicing tree is labeled as H, all the rectangles belonging to the left sub-tree rooted at N_k should be placed below any rectangles belonging to the right sub-tree rooted at N_k . Similarly, if any internal node N_k in the slicing tree is labeled as V, all the rectangles belonging to the left sub-tree rooted at N_k should be placed to the left of any rectangle belonging to the right sub-tree rooted at N_k . For example, Figure 4 shows a slicing tree and two different corresponding placements.

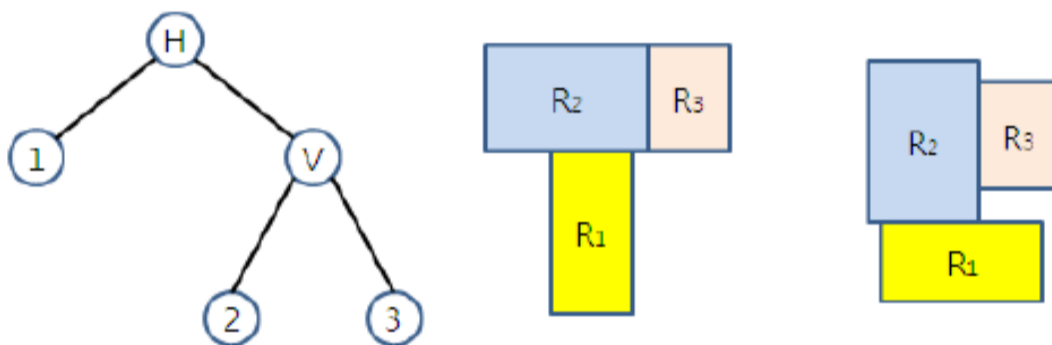


Figure 4. A slicing tree and its two different corresponding placements

Let \mathbf{R} denote the minimum rectangle which encloses all the n rectangles when a placement is determined. Such enclosing rectangles corresponding to the placements shown in Figure 4 are shown in Figure 5 with dotted lines.

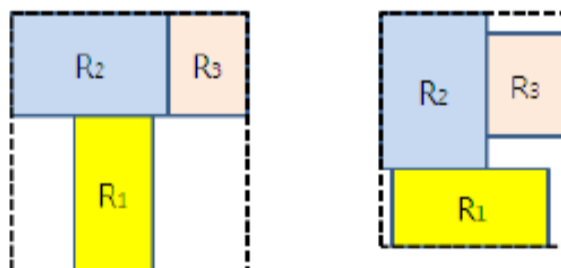


Figure 5. Minimum enclosing rectangles

Given information regarding a slicing tree and the rectangles’ dimensions, your program should determine the location for each rectangle such that the placement meets the given constraints and such that the area of the enclosing rectangle \mathbf{R} is as small as possible. Notice that the area of the enclosing rectangle \mathbf{R} can be affected by the orientation of each rectangle.

Input

Your program is to read from standard input. The input consists of T test cases. The number of test cases T is given in the first line of the input. Each test case starts with a line containing an integer n ($1 \leq n \leq 1,000$), where n is the number of rectangles. In the following lines, dimensions of n rectangles are given, each line for each rectangle. The i -th ($1 \leq i \leq n$) line contains two integers, w and h ($1 \leq w, h \leq 500$), w for width and h for height of rectangle i . In the next line the information regarding a slicing tree is given. The information is represented as a list consisting of $2n - 1$ items separated by spaces, which is obtained by traversing the slicing tree in post-order. Each item of the list is either a label for an internal node or for an external node. The label for an internal node is either ‘H’ or ‘V’. The label for an external node is an integer i ($1 \leq i \leq n$), which is the rectangle identification number.

Output

Your program is to write to standard output. Print exactly one line for each test case. For each test case, find a placement such that the given constraints regarding relative locations among rectangles are satisfied and the area of the enclosing rectangle \mathbf{R} is as small as possible. Then print the area of the rectangle \mathbf{R} for each test case. You can assume that the resulting area of \mathbf{R} is less than 10^9 for each test case.

The following shows sample input and output for two test cases.

Sample Input

```
2
5
1 5
4 2
3 3
1 3
5 4
2 1 V 3 5 H 4 V H
6
4 2
5 7
7 2
4 4
1 4
5 3
2 3 H 1 5 V 6 4 H V V
```

Sample Output

```
65
105
```